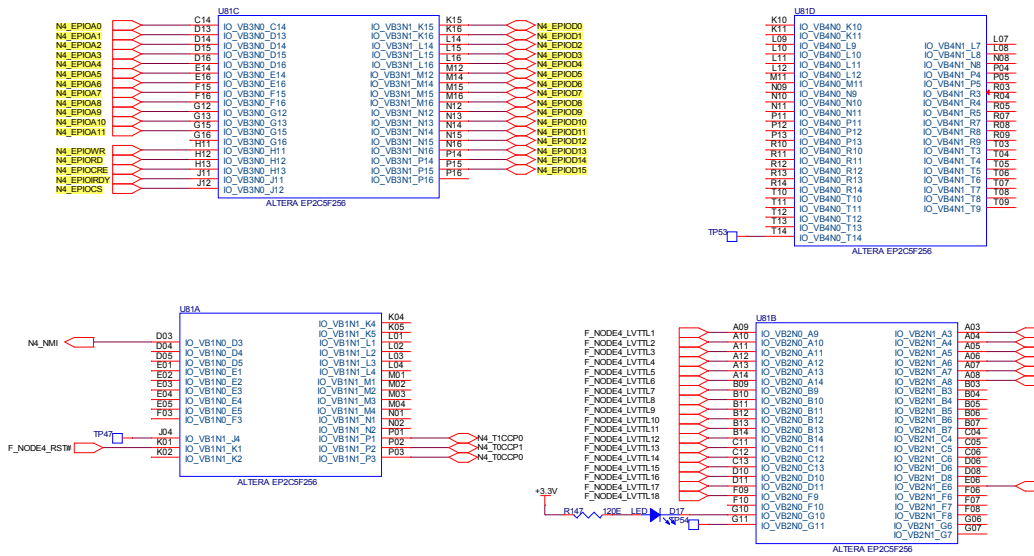
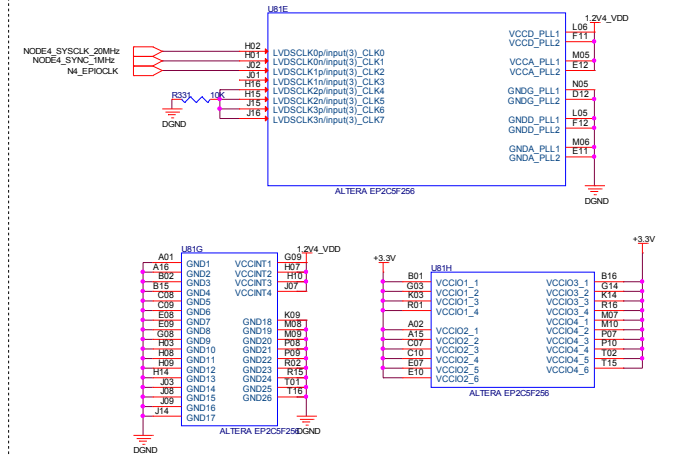


FPGA Interface

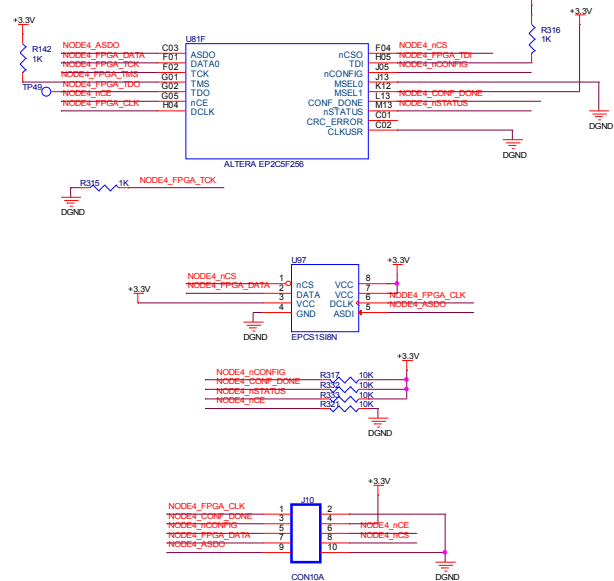
EPI Host Bus x16



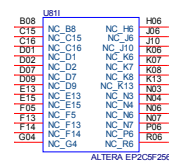
FPGA Power and CLK Interface



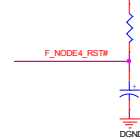
FPGA EEPROM Interface



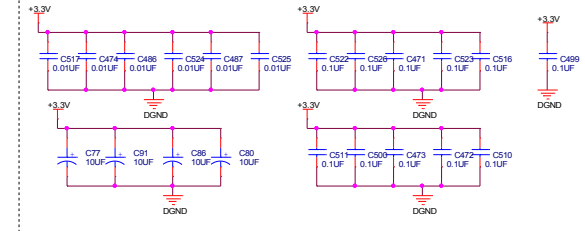
FPGA NC Pins



RESET CIRCUIT



3.3V Decoupling Capacitor



1.2V Decoupling Capacitor

